IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| in te Applica | uion oi: | <u> </u> | |
|--|-----------------------|--|------------------|
| | Ali Muhtaroglu et al. | Examiner: Not yet assigned Art Unit: Not yet assigned | |
| Serial No.: | Not yet assigned | | Not yet assigned |
| Filed: | Herewith | ,) | |
| For: METHOD AND APPARATUS FOR ON-DIE VOLTAGE FLUCTUATION DETECTION | | ,)) | |
| Which is a Continuation of Application of: | | | |
| Serial No: | 10/021,055 | | • |
| Filed: | December 19, 2003 | <u>}</u> | |

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

FORMAL DRAWINGS SUBMISSION

Enclosed herewith for submission in the United States Patent and Trademark

Office are eight (8) sheets of formal drawings for the patent application referenced above.

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